

Pilot Overview

Review September 2011

Josef Noll (Movation), based on contributions from partners

ARTEMIS Call 2009 - SP6100204

































Overview - prototypical demonstrations



- SPD levels are achieved through specific configurations by the overlay
- Composability Middleware prototype
- Monitoring trains with WSNs
- FPGA Power Node Prototype
- Cognitive Radio Node Prototype

Also prototypes for

- pSHIELD semantic model prototype (ontology)
- Policy-based management and hybrid-automata model

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ARTEMIS Call 2009 Project

p.S.HI.E.L.D.

ARTEMIS

pilot embedded Systems arcHltecturE for multi-Layer Dependable solutions

EXECUTIVE summary

To help safeguard society, pSHIELD will guarantee the privacy and security of embedded systems by making these built-in features of future designs.

RELEVANCE CALL 2008 objectives

The SHIELD consortium proposes a compact R&D, or pilot, project (pSHIELD) to address the core concepts of SHIELD. The pilot is intended to be a pioneer investigation enhanced with R&D activities that will be proposed in the future ARTEMIS Calls.

MARKET innovation

The project will have a great impact on the 100 ket of the ES. By addressing the reusant too'c of bill and designed solutions, the interoperability of as one of \$1.00 ket, aclogies and the standardsed SPD certifiability, it is possile to the thriate an overall 30% cost reduction for a full SHIB's original design verthodology. The composability of the SHELD architicist. (for sevork will have great impact on the system design costs and time jot hardrest of new SPD solutions in ES. The integrated use of SPD getrics will have impact on the development because the qualification, (re-)certification and (re-)validation process of a SHELD framework instance will be faster, easier and widely accepted.

TECHNICAL innovation

pSHELD will approach SPD at 4 different levels: node, network, middleware and overlay. For each level, the state of the art in SPD of single technologies and solutions will be improved and integrated (hardware and communication technologies, cryptography, middleware, smart SPD applications, etc.). The SPD technologies will be enhanced with composable functionality to incorporate the pSHIELD architectural framework.

The pSHIELD project will be focused on:

- > Demonstrate composability
- > New technologies
- > Modularity and expandability
- Innovative, modular, composable, expandable and highly dependable architectural framework
- > Metric
- > Validation of the SHIELD integrated system in one application scenario.





Advanced Research & Technology for EMbedded Intelligence and Systems

Pilot: Semantic Overlay and Composability



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- SPD levels are achieved through specific configurations by the overlay
 - demonstrating the behaviour of the pSHIELD middleware
 - demonstrating SPDdriven composability
 - using metricsformulation from WP2



Pilot: Nano-Micro-Personal-M2M platform

File Edit View Search Terminal Help

- Nano-Micro-Personal-M2M Platform
 - security interworking between embedded sensors and Telecom service platform
 - Identify SPD functions in an integrated embedded sensor testbed
 - opens for SPD metrics based composability



Pilot: Monitoring trains with WSNs



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Monitoring trains with WSNs

identity requirements of real-world applications

 Identify SPD functions in an integrated embedded sensor testbed

opens for SPD metrics based

composability



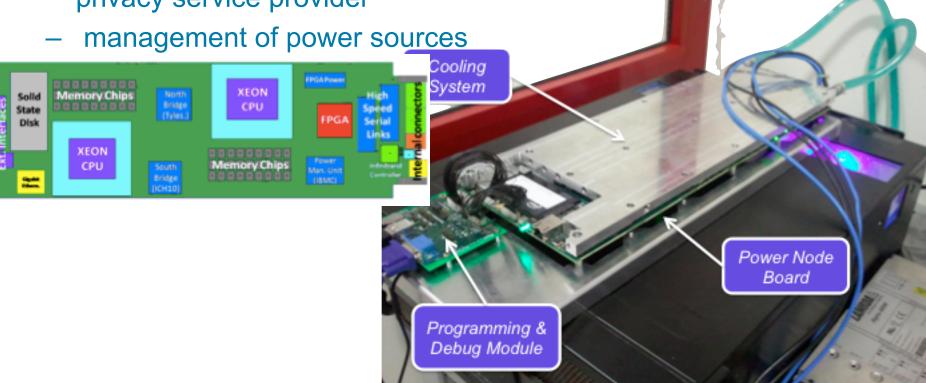
Pilot: FPGA Power Node Prototype



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- FPGA Power Node Prototype
 - modular system reconfiguration
 - self-dependability at node layer

hardware and software security and privacy service provider



Pilot: Cognitive Radio Node Prototype



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- Cognitive Radio Node Prototype
 - reconfigurable radio components with waveform Tx parameters
 - Sensing mechanisms to acquire awareness about resources
 - Cognitive algorithms elaborating available resources
 - Embedded platform adaptation for validation of algorithms



PCB OMBRA-pSHIELD – OMAP uP (18x68 mm) WCP (1K pieces) =~150 Euro Computational Power 5X

Life pilots



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let us walk room the demo and get something to eat